

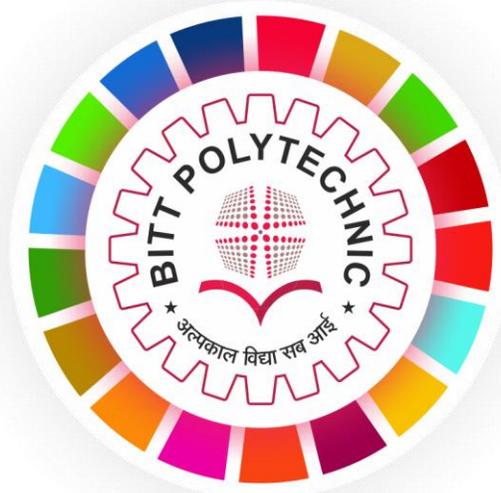
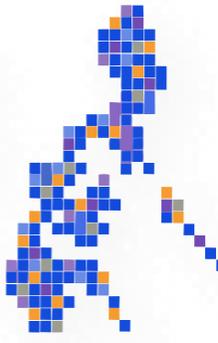
BITT Group of Institutions Celebrating it's 27 Glorious years

BITT POLYTECHNIC

(Approved by AICTE, New Delhi & Affiliated to Jharkhand University of Technology, Ranchi)

RUN AND MANAGED BY BIRSA INSTITUTE OF TECHNOLOGY (TRUST) - BITT

मानवता की सेवा, सभी के लिए सर्वश्रेष्ठ शिक्षा



UNIT OF BITT

हमारे सभी विद्यार्थियों एवं सहयोगियों को
हमारी 27 वर्षों की गौरवशाली यात्रा का हिस्सा
बनने के लिए धन्यवाद.

आपका विश्वास ही हमारी निरंतर सफलता की प्रेरणा है।

सर्वश्रेष्ठ पॉलीटेक्निक संस्थान, सर्वोत्तम पढ़ाओ, सर्वोत्तम बनाओ।

DEPARTMENT OF TRAINING, PLACEMENT, CONSULTING,
INNOVATION & RESEARCH (DTPCIR)

BITT-G/NOTICE/2022-23/ 10690

Dated: 21/03/2023

NOTICE

All the students of diploma on EE, ECE & CSE branch of sessions: 2020-23, 2019-22, 2018-21 are hereby informed that **"BHIVE DESIGN"** is shortly going to do online/ offline /recruitment/ Apprenticeship/ placement program.



BHIVE DESIGN

Founded in 2018, BHIVE is a semiconductor IC design services company headquarter at Bangalore and design centre at Ranchi. We provide IC design services to the semiconductor industries in India and abroad. BHIVE Design team has vast experience in Digital front-end Design & Verification, Analog / RF, mm Wave Custom Layout, Standard cells. iOS, ESD Memory layout development, Physical Design, Automation in Perl & Skill, Embedded & Firmware.

BHIVE Design team has vast experience in Digital Front-end Design & Verification, Analog / RF, mm Wave Custom Layout, Standard cells. iOS, ESD Memory layout development, Physical Design, Automation in Perl & Skill, Embedded & Firmware. 'BHIVE Design' is professionally managed and supported by experienced specialists and consultants having formal qualifications and industrial experience. The company was incorporated with the dream of serving excellence with perfection. The company has gained the strong clientele with long term relationship of service. Today, the company is well - known for the quality services with expected quality and time bound delivery with reasonable rate quotations. Deciding to outsource a Digital Front-end design can be a very tough task. Between choosing a design firm to handle the design, knowing what to give them in terms of inputs, ensuring the design meets your specifications, there are lot of opportunities for a problem to arise. Upon deciding who you will be outsourcing the design too, the creation process begins. There are a number of critical steps which must be met for successful completion of a design. Here we'll try and provide insight to the design process, as well as what is required of both the customer and designer at major milestones within the process. BHIVE Design is always want to getting it right for the first time with our checklist and workflow structure. We have exhaustive white box techniques to ensure all the entrapped errors are corrected before the designs is brought to you. It resulted into saving your time and cost on design. BHIVE Design team has vast experience in Digital Front-end Design & Verification, Analog / RF, mm Wave Custom Layout, Standard cells. iOS, ESD Memory layout development, Physical Design, Automation in Perl & Skill, Embedded & Firmware.

JOB DETAIL:

Position Name: IC Chip/Design Engineer/IC Mask Design

Location: Design Center at Ranchi

Educational Qualification: Diploma in EE, ECE & CSE.

Diploma passing year: sessions: 2020-23, 2019-22, 2018-21

Salary: During the training period (6 months) they will be eligible for stipend (Rs 10,000 per month) and other basic facilities.

After successful completion of training, they will be hired as an employee of BHIVE Design Pvt. Ltd with a **CTC** package of 2.5 Lakhs per annum and they will be eligible for other standard facilities of the Company.

Job Location - Ranchi

Vacancy: Multiple

Last date of online registration is **24/03/2023.**

RECRUITMENT PROCESS:

Online/ offline / Apprentices/recruitment/ placement program

Evaluation Process: Offline /face to face interview

Campus Venue: BITT Polytechnic, Ranchi

Campus Date & Time: 25/03/2023 (Saturday), Reporting Time: 10:00 a.m.

Written Exam (To understand the basic Electrical/ Network Theory and Basic Electronics Knowledge of candidates)

Duration: 1 hour

Interview: Selected candidates will be having a face to face Technical interview for 5 to 10 minutes followed by HR interaction.

DOCUMENTS REQUIRED (In Joining):

1. PAN Card
2. AADHAR Card
3. 10th /12thMark sheet
4. Board Certificate
5. Diploma Semester wise Mark sheet and Certificate.

NOTE:

- a. The interested students should fill their details at available Link: <https://forms.gle/zB9zFtWfdQM4q5GK6> on or before 24/03/2023 up to 11:30 PM, for attending the online placement program.
- b. The above notice has been shared from the authentic source like advertisements/print media/newspaper/ journals/recruiters/ public/ private sectors. The aim of the institute is to make an advanced level awareness of the vacancies in public as well as private sectors for recruitments/ placements of diploma students as per company's recruitment policies. The students have to complete the registration process on visiting the company website (career section) Or fill up the recruiter's form on their links as well as they have to fill the college registration form in complete manner as per company norms/HR policies. The recruitment/ placement programs are based on online/ offline tests and interviews conducted by authorized officials. The placements/joining will be only offered to those students who will qualify all the process. The college is helping hand for students and taking best possible efforts for their brighter future. Remember, there is no any shortcuts / easy way to have direct placements. The college is not fully responsible to the students who will not able to get selected in the desired company.

BITT HELP-DESK:

BITT Special Student's Care: In Case of any issue, for any Observation/Suggestion/Grievances/Feedback, Please WhatsApp on 9931080111 / Call 9470193650 Or Email at: rkbitt@gmail.com (From 11.30 AM to 02.30 PM & Days: MONDAY - FRIDAY Only). We are committed for best academic support to our students.

(NOTE - All students are advised to make only WhatsApp messages on BITT helpline WhatsApp No.- 9931080111. Students may send their feedbacks/suggestions/complaints through WhatsApp only. No any call will be entertained on this no. in any cases.)

BIRSA INSTITUTE OF TECHNOLOGY (TRUST)- BITT: Indeed, it is pride moment for all of us that The BITT Group of Institution is Celebrating it's 27 Glorious Years in field of Technical Education, Scholarship & Serving the Nation in the excellent Way. BITT Polytechnic - **मानवता की सेवा, सभी के लिए सर्वश्रेष्ठ शिक्षा** (Approved by AICTE, NEW DELHI & Affiliated to Jharkhand University of Technology) - **सर्वश्रेष्ठ पॉलिटेक्निक संस्थान, सर्वोत्तम पढ़ाओ, सर्वोत्तम बनाओ** We firmly believe in "Student Centric Approach. So, Be Proactive, Not Reactive". Your cooperation is highly solicited in this regard.

COVID Guidelines: the college has no responsibility in case any of the student, visitors (visiting office) get corona infected the college is following all the COVID protocols keeping the spread of CORONA.



Hiring Process



Outline

1. Industry Overview
2. About Us
3. The Team
4. Our Services
5. Current Vacancies
6. Job Description
7. Recruitment Process
8. Recruitment Funnel
9. Growth Prospects
10. Contact Details



Thought of the day!

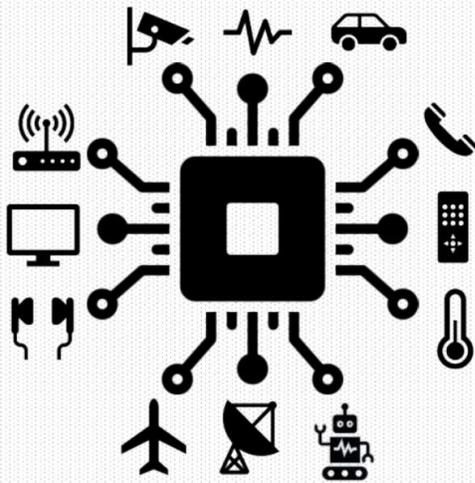
IT IS THE TIME TO PICK BETWEEN

A REGULAR OPPORTUNITY

&

AN EXCELLENT OPPORTUNITY

1. Industry Overview



What is the industry?

SEMICONDUCTOR

Semiconductors are the brain of modern electronics

The **Semiconductor** industry designs and manufactures semiconductors which are tiny electronic devices made from **Silicon**, **Germanium**, or gallium arsenide compounds.

Semiconductors are found in just about every electronic device like televisions, computers, medical diagnostic equipment, cell phones, and video games. It enables advances in communications, computing, healthcare, military systems, transportation, clean energy, and countless other applications.

In the production process of IC chips, the designed circuit diagram is first transferred to the semiconductor wafer. Through a series of procedures, an integrated circuit (IC, Integrated Circuit) is formed on the surface of the wafer, which is then cut into a piece called Dies. These Dies are finally wrapped in a protective shell, forming the final chip.

The upper, middle, and lower reaches of the semiconductor supply chain

- Upstream: IC design. Production of IC design drawings.
- Midstream: IC manufacturing. The actual manufacturing of the integrated circuit on the design drawing.
- Downstream: IC packaging and testing. After passing testing, the wafer is cut into bare crystals, and the shell is added to package the chips.

Types of Semiconductor Companies

FABLESS – COMPANIES THAT FOCUS ON DESIGN

- QUALCOMM
- BROADCOM
- NVIDIA
- MEDIA TECH

FOUNDRIES – COMPANIES THAT FOCUS ON MANUFACTURING

- INTEL
- SAMSUNG
- TSMC
- IBM

Key facts (Global)

- In 2021, global semiconductor industry sales reached a record \$555.9 billion, up 26.2% year on year, the U.S.-based Semiconductor Industry Association (SIA) said.
- The SIA said that demand is expected to “rise significantly” in the coming years.
- China remained the biggest market with sales there totalling \$192.5 billion in 2021.
- Demand for semiconductor production is projected to rise significantly in the years ahead, as chips become even more heavily embedded in the essential technologies of now and the future.

Key facts (Domestic)

- In 2019, Prof Vikram Kumar, emeritus professor in physics at IIT Delhi revealed that India is spending more money on import of semiconductor than on oil.
- Result was the addition of semiconductors in the drive known as #makeinIndia
- Market size: India's electronics market, one of the largest in the world in terms of consumption, has grown to USD 400 billion by 2020 from USD 69.6 billion in 2012
- India has pursued a two-pronged strategy of import substitution and export encouragement, through the Make in India campaign coupled with the Digital India campaign, along with the Start-up India and the Skill India campaigns.
- The government has fostered an environment conducive to foreign direct investment (FDI) inflow in a number of ways, as outlined in the National Electronics Policy and the National Telecom Policy.

2. About



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**Bhive-Design Infrastructure
is now ISO 27001:2013 Certified**

3. The **TEAM**-Togther **Everyone** **Achieves** **More**



We have a perfect blend of Leadership, Experience, Talent and Positive Attitude in our ever growing TEAM.

Stalwarts from the industry are leading/mentoring the organization with a sharp focus and well calculated path for every individual associated with.

The favourite words of our leadership is **FOCUS, LEARN & NOW**

...and when we are done with work, it only **FUN**.

A BIG SMILE is our main official attire.

DO YOU THINK YOU FIT IN?

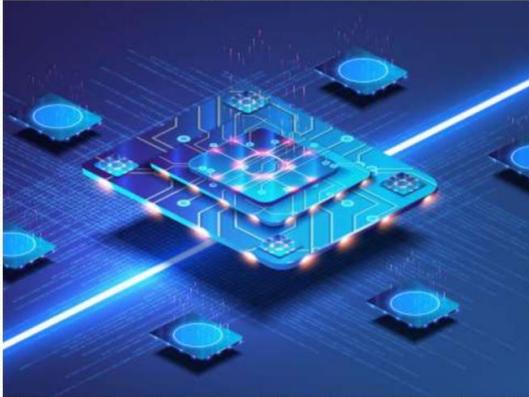


We have the habit to turn challenges into opportunities every time with our inventiveness, tenacity, and passion. Together, we break new ground and answer the questions nobody has thought or dared to ask.

4. Our Services

- 1. Digital Front-end design & Verification**
- 2. Physical Design (Netlist to GDS)**
- 3. IC Mask & Layout Design**

What we do?



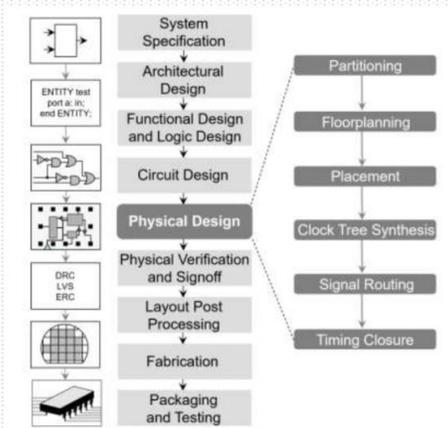
1. Digital Front-end design & Verification

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..What we do?



2. Physical Design (Netlist to GDS)

Physical design is based on a netlist which is the end result of the synthesis process. Synthesis converts the RTL design usually coded in VHDL or Verilog HDL to gate-level descriptions which the next set of tools can read/understand. This netlist contains information on the cells used, their interconnections, area used, and other details. Typical synthesis tools are:

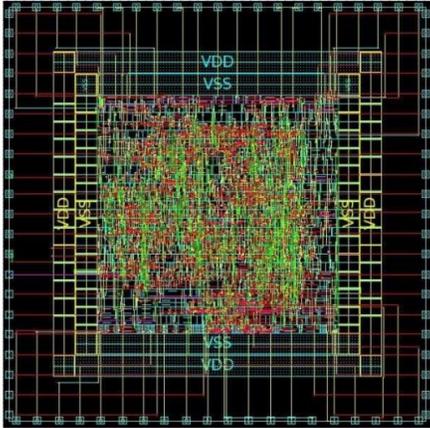
1. Cadence RTL Compiler/Build Gates/Physically Knowledgeable Synthesis (PKS)
2. Synopsys Design Compiler

During the synthesis process, constraints are applied to ensure that the design meets the required functionality and speed (specifications). Only after the netlist is verified for functionality and timing it is sent for the physical design flow.

GDS (Graphic Data Stream) is a file that was developed by calma company in the year 1971 and the GDS II in the year 1978.

It is a binary file format that represents layout data in a hierarchical format.

..What we do?



3. IC Mask & Layout Design

Integrated circuit layout, also known as IC layout, IC mask layout, or mask design, is the representation of an integrated circuit in terms of planar geometric shapes which correspond to the patterns of metal, oxide, or semiconductor layers that make up the components of the integrated circuit.

When using a standard process—where the interaction of the many chemical, thermal, and photographic variables is known and carefully controlled—the behaviour of the final integrated circuit depends largely on the positions and interconnections of the geometric shapes. Using a computer-aided layout tool, the layout engineer—or layout technician—places and connects all of the components that make up the chip such that they meet certain criteria—typically: performance, size, density, and manufacturability. This practice is often subdivided between two primary layout disciplines: Analog and digital.

Thive Design team has vast experience in Digital front-end Design & Verification, Analog / RF, mm Wave Custom Layout, Standard cells, iOS, ESD Memory layout development, Physical Design, Automation in Perl & Skill, Embedded & Firmware.

What we do:

• IC Chip Design

System-level design and microarchitecture planning

• Analog / RF, mm Wave Custom Layout

Automated design platforms for complex ICs

• ESD Memory layout development

System Level ESD Analysis of Memory Devices

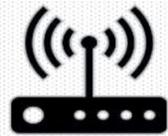
• Physical Design (Netlist to GDS)

Physical designers are responsible for producing high quality design tape out.

We work for the following industries:



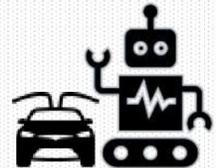
COMMUNICATION



CONSUMER ELECTRONICS



MEDICAL SCIENCE



AUTOMATION

Our humble achievements so far..

- 100 and growing happy customers



- 50+ completed projects



- 140 Design ideas



- *Contact our company CA for the revenue/turnover/profits/growth data!*



Upcoming Campuses

We have 2 upcoming state of the art campuses in the following locations:

Bhubaneswar

Hyderabad



5. Current Vacancies

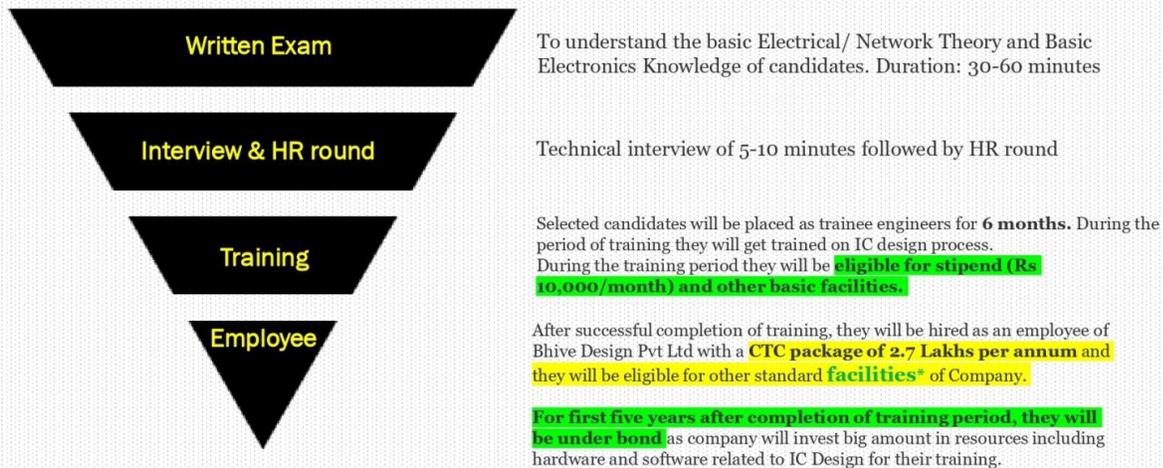
Chip Design Engineer

Current Opening/Requirement is for Design Center at **Ranchi**. We are looking for 2019/2020/2021 & 2022 pass out B.Tech candidates of the following branches:

- Electrical Engineering (EE)
- Electrical & Electronics (EEE)
- Electronics & Communication (EC)
- Computer Science (CSE)



8. Recruitment Funnel



*Great works are performed not by strength but by **Perseverance** – Samuel Johnson*

*Facilities

- We provide home style cooked food and snacks for all our employees, both lunch & dinner (whichever is applicable)
- Safety is one of our top priority (Hygiene and Safety standards are always maintained and monitored by the mentors)
- Personal celebrations are also celebrated in our office
- Soft skills & personality development workshops are often held for the growth of the employee



9. Growth Prospects



- GUIDANCE
- PERSONAL
- PROFESSIONAL
- FINANCIAL GROWTH
- PROMOTIONS

Dreams are very important. You can't do it unless you imagine it – George Lucas

10. Contact Details



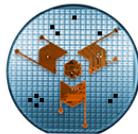
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career@bhive-design.com



www.bhive-design.com



BHIVE-DESIGN
INNOVATIVE MINDS AT WORK

NAME: _____

PERMANENT ADDRESS: _____

FATHER'S OCCUPATION: _____

MOTHER'S OCCUPATION: _____

NO. OF SIBLINGS: _____ ARE THEY WORKING? Y/N _____

WHICH ORGANIZATION ARE THEY WORKING FOR? _____

FROM WHERE DID YOU HEAR ABOUT BHIVE DESIGN? _____

DO YOU AGREE WITH THE COMPANY POLICY BOND OF 5 YEARS? _____

DO YOU AGREE TO WORK IN SHIFTS? _____

BTECH STREAM: _____

PASSING YEAR: _____

COLLEGE: _____

DATE: __/__/_____

SIGNATURE